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December 12, 2000

**BOX PCT**Assistant Commissioner for Patents  
Washington, D.C. 20231

PCT/JP99/07387

-filed December 28, 1999

Re: Application of Yoshihisa FURUTA & Yoshinori WATANABE  
METHOD OF RESIN ENCAPSULATING SEMICONDUCTOR CHIP AND PRESSURE-  
SENSITIVE ADHESIVE TAPE FOR ADHESION TO LEADFRAME AND THE LIKE  
Our Ref: Q62228

Dear Sir:

The following documents and fees are submitted herewith in connection with the above application for the purpose of entering the National stage under 35 U.S.C. § 371 and in accordance with Chapter I of the Patent Cooperation Treaty:

- ☒ an executed Declaration and Power of Attorney.
- ☒ an English translation of the International Application.
- ☒ 2 sheet(s) of formal drawings.
- ☒ an executed Assignment and PTO 1595 form.
- ☒ International Search Report, Information Disclosure Statement and form PTO 1449.

It is assumed that copies of the International Application, the International Search Report, the International Preliminary Examination Report, and any Articles 19 and 34 amendments as required by § 371(c) will be supplied directly by the International Bureau, but if further copies are needed, the undersigned can easily provide them upon request.

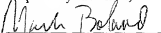
The Government filing fee is calculated as follows:

Total claims	4	-	20	=		x	\$18.00	=	\$72.00
Independent claims	1	-	3	=		x	\$80.00	=	\$80.00
Base Fee									\$860.00
Multiple Dependent Claim Fee									\$270.00
<b>TOTAL FILING FEE</b>									<b>\$1130.00</b>
<b>Recordation of Assignment</b>									<b>\$ 40.00</b>
<b>TOTAL FEE</b>									<b>\$1170.00</b>

Checks for the statutory filing fee of \$1130.00 and Assignment recordation fee of \$40.00 are attached. You are also directed and authorized to charge or credit any difference or overpayment to Deposit Account No. 19-4880. The Commissioner is hereby authorized to charge any fees under 37 C.F.R. §§ 1.16, 1.17 and 1.492 which may be required during the entire pendency of the application to Deposit Account No. 19-4880. A duplicate copy of this transmittal letter is attached.

Priority is claimed from April 12, 1999 based on JP Application No. 11-103559.

Respectfully submitted,



Mark Boland

Registration No. 32,197

MXB/sib

2/pits

# Description

METHOD OF RESIN ENCAPSULATING SEMICONDUCTOR CHIP AND PRESSURE-SENSITIVE ADHESIVE TAPE FOR ADHESION TO LEADFRAME AND THE LIKE

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## Technical Field

This invention relates to a method of encapsulating a semiconductor chip with a resin and a pressure-sensitive adhesive tape used therein for adhesion to a leadframe and the like.

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## Background Art

In the manufacture of packaged semiconductor devices such as QFP, a semiconductor chip is bonded on a leadframe, put in a cavity of a mold, and encapsulated with a resin by transfer molding.

15

It is necessary to secure a fairly wide contact area between the mold and the leadframe in order to prevent resin from leaking from the contact interface. The ratio of the contact area to the cavity space should be considerably high. It follows that the number of chips encapsulated in a single shot with a mold of a size is reduced, which is disadvantageous for working efficiency.

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There is also a fear that the chip moves with the resin flowing into the mold to break the bonding parts. The mold structure is therefore unavoidably complicated to eliminate this.

25

It is an object of the present invention to provide a method of encapsulating a semiconductor chip with a resin which achieves improved efficiency of resin encapsulating while preventing breakage of bonding parts without fail.

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Disclosure of the Invention:

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The resin encapsulating method for a semiconductor chip according to the present invention comprises adhering a pressure-sensitive adhesive tape to a leadframe or a film carrier tape, bonding a semiconductor chip to the leadframe or carrier tape having the pressure-sensitive adhesive tape, encapsulating the semiconductor chip with a resin in a mold, and stripping the pressure-sensitive adhesive tape, wherein the pressure-sensitive adhesive tape has a thermal shrinkage of 3% or less on resin encapsulating. The expression "a thermal shrinkage of 3% or less on resin encapsulating" as used herein is intended to mean that the thermal shrinkage of the tape in at least one of the width and the lengthwise directions is 3% or less at a encapsulating temperature.

Brief Description of the Drawings

Figs. 1A to 1E represent an example of the method of encapsulating a semiconductor chip with a resin according to the present invention.

Fig. 2 shows the leadframe in Figs. 1A to 1E.

Fig. 3 is a cross section of Fig. 1C taken along line III-III.

Best Mode for Carrying out the Invention

The practice of carrying out the invention will be described hereunder by referring to the drawings.

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Figs. 1A to 1E is a drawing showing an example of the method for encapsulating a semiconductor chip with a resin according to the present invention.

Fig. 1A shows a leadframe A with an pressure-sensitive adhesive tape adhered thereto, wherein numeral 1 is a leadframe having a group of units each having a large number of stitches 11 arranged around the device hole. Numeral 2 is an pressure-sensitive adhesive tape affixed to the leadframe 1, whose thermal shrinkage on resin encapsulating is controlled to 3% or less, preferably 2% or less, still preferably 1% or less. Numerals 21 and 22 are a pressure-sensitive adhesive layer and a substrate, respectively.

In producing resin-encapsulated semiconductor chips according to the present invention, semiconductor chips 3 are placed and fixed in the device holes, one by one, of the leadframe A with the pressure-sensitive adhesive tape adhered thereto, and the chip 3 and the stitches 11 of the leadframe 1 are bonded with a bonding wire 31 as shown in Fig. 1B. In the step of resin encapsulating, the semiconductor chip 3 is placed in each cavity 41 of a mold 4 and encapsulated with a resin by transfer molding as shown in Fig. 1C. Then as shown in Fig. 1D, the pressure-sensitive adhesive tape is stripped off the leadframe 1, and the leadframe is cut and trimmed to obtain a resin-encapsulated semiconductor chip shown in Fig. 1E.

Fig. 3 shows the contact interface between the mold and the leadframe. The stitches 11 of the leadframe bite into the pressure-sensitive adhesive layer 21 of the pressure-sensitive

adhesive tape 2 so that the pressure-sensitive adhesive layer 21 and the stitches 11 are co-planar. The pressure-sensitive adhesive layer 21 among the stitches adheres to the mold 4 and acts as a sealant to exhibit high encapsulating properties. Therefore, the encapsulating effect can be exerted by increasing the thickness of the pressure-sensitive adhesive layer 21. Where the encapsulating resin fills deep into the gaps between stitches 11, a sufficient encapsulating effect can be exhibited by increasing the mold pressure or adhesive strength. Such manipulations can be selected according to the end use.

The pressure-sensitive adhesive tape shrinks during resin encapsulating because it is also heated by the heat of the mold.

A fear is entertained that the thermal shrinkage of the pressure-sensitive adhesive tape may cause a shear stress in the interface between the leadframe with the adhesive tape and the mold, and a high shear stress would deteriorate the encapsulating properties due to the shearing strain. In the present invention, since the pressure-sensitive adhesive tape used has a thermal shrinkage of not more than 3%, preferably 2% or less, still preferably 1% or less, on resin encapsulating, the above-described high encapsulating performance can be secured during the resin encapsulating.

Accordingly, even though the contact interface between the mold and the leadframe is made smaller, sufficient encapsulating can be achieved. With the smaller contact interface, the number of cavities per mold can be increased so that the number of chips encapsulated per shot can be increased. Since the chips are fixed

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onto the pressure-sensitive adhesive layer, the chips can be stably maintained in their fixed state against the resin flow thereby preventing breakage of the bonding parts.

The pressure-sensitive adhesive strength of the pressure-sensitive adhesive tape is desirably as high as possible for encapsulating the contact between the mold and the leadframe but is desirably as low as possible for strippability from the leadframe after resin encapsulating. It is preferred to use a pressure-sensitive adhesive tape having a pressure-sensitive adhesive strength to the leadframe of 400 gf/20 mm or less, preferably 300 gf/20 mm or less and 5 gf/20 mm or more, at 23°C.

If the pressure-sensitive adhesive strength is less than 5 gf/20 mm, the pressure-sensitive adhesive tape tends to separate during use.

The heating conditions for resin encapsulating is usually about 180°C. It is a matter of course that the pressure-sensitive adhesive tape is required to have heat resistance to the heating temperature.

The substrate of the pressure-sensitive adhesive tape can be of heat-resistant materials satisfying the above-described various conditions, for example, heat-resistant plastic films of polyimide, polyphenylene sulfide, etc., glass cloth, and the like. Where the heating condition of the resin encapsulating is 150°C or lower, a polyethylene terephthalate film may be used.

The pressure-sensitive adhesives of the pressure-sensitive adhesive tape include not only acrylic pressure-sensitive adhesives and silicone pressure-sensitive adhesives

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but also epoxy adhesives as far as the above-described various conditions are met. It is preferred to use silicone pressure-sensitive adhesives having excellent heat resistance. In order to satisfy the above-specified requirement in terms of peel strength, heat-resistant fillers (such as glass beads, various inorganic fillers, and heat-resistant organic fillers) can be added to the pressure-sensitive adhesive if desired. It is also possible to use a foaming adhesive which foams by the heat of resin encapsulating to reduce the peel strength to 400 gf/20 mm-tape width or less.

The thickness of the substrate is from 5 to 250  $\mu\text{m}$ , preferably 5 to 100  $\mu\text{m}$ . If the thickness is smaller than 5  $\mu\text{m}$ , the adhesive tape tends to be fractured, torn, or blistered, causing reduction of workability. If it exceeds 250  $\mu\text{m}$ , the heat conduction efficiency from the mold to the resin is reduced.

The thickness of the pressure-sensitive adhesive layer is from 2 to 100  $\mu\text{m}$ , preferably 5 to 75  $\mu\text{m}$ . If the thickness is less than 2  $\mu\text{m}$ , there is a difficulty in coating, and an adhesive failure or blisters are liable to be caused. If it exceeds 100  $\mu\text{m}$ , there is a difficulty in coating, and the adhesive tends to be pressed out from the edges, which attracts foreign matter and gives off increased gas on heating.

The pressure-sensitive adhesive layer can be crosslinked appropriately in order that it can be stripped by interfacial separation without leaving any adhesive due to cohesive failure or in order to control the adhesive strength.

In order to prevent delamination between the substrate and

the pressure-sensitive adhesive layer, the substrate can be subjected, if desired, to primer coating or surface roughening, for example, sputter etching.

5 In order to increase the heating efficiency in the resin encapsulating, heat conductive particles, such as boron nitride, can be added to the pressure-sensitive adhesive layer.

10 Frictional electricity is generated during the production of the semiconductor devices. In case there is a fear of the chip's destruction due to an electric shock, the substrate can be of an electrically conductive material, and the pressure-sensitive adhesive layer can be made electrically conductive by addition of conductive particles, such as carbonickel or carbon black.

15 The present invention is applicable to a TAB system. In this case, the pressure-sensitive adhesive tape is stuck to the back side of a film carrier tape (e.g., a polyimide film having copper foil finger leads), and semiconductor chips are disposed and fixed one by one in the device holes of the carrier tape with the pressure-sensitive adhesive tape. Each of the chips and the finger leads of the film carrier tape are connected by Au-Sn eutectic bonding. In the step of resin encapsulating, the semiconductor chips are put in the respective cavities of a mold and encapsulated with a resin by transfer injection molding. The film carrier tape is then trimmed to obtain resin-encapsulated semiconductor chips.

25 The present invention will now be illustrated in greater detail by way of Examples and Comparative Examples, but the present invention is not deemed to be limited thereto.

Comparative Example 1

Chips bonded to a leadframe (Cu-based; number of stitches: 100) having no pressure-sensitive adhesive tape were put in a mold and encapsulated with a resin at 200°C and 20 kg/cm<sup>2</sup>. As a result, resin leaks occurred.

Example 1

A platinum catalyst (0.5 part by weight) was uniformly mixed into 100 parts by weight of a silicone pressure-sensitive adhesive. The mixture was applied to a 25 μm thick polyimide film and dried by heating at 130°C for 5 minutes to prepare a pressure-sensitive adhesive tape having a pressure-sensitive adhesive layer of 10 μm in thickness.

The pressure-sensitive adhesive tape had a thermal shrinkage of 0.5% or less at 200°C and, when adhered to a leadframe, showed an initial pressure-sensitive adhesive strength of 60 gf/20 mm at 23°C, a pressure-sensitive adhesive strength of 180 gf/20 mm after 200°C x 1 hour, and a pressure-sensitive adhesive strength of 200 gf/20 mm after 200°C x 5 hours. The thermal shrinkage was determined by allowing a 300 mm long and 19 mm wide tape sample marked with two gauges at a distance of 200 mm to stand at 200°C for 2 hours and, after allowing the sample to cool to 23°C, measuring the distance between the two gauge marks.

The thermal shrinkage was obtained as [(distance between two gauge marks before shrinkage - distance between two gauge marks after shrinkage)/distance between two gauge marks before shrinkage] x 100 (%).

The pressure-sensitive adhesive tape was stuck to the same

leadframe as used in Comparative Example 1, and resin encapsulating was carried out in the same manner as in Comparative Example 1.

The pressure-sensitive adhesive tape was then peeled. No resin leak nor deformation of the leadframe was observed.

5 Example 2

Resin encapsulating was carried out in the same manner as in Example 1, except for using an available silicone pressure-sensitive adhesive tape (pressure-sensitive adhesive layer thickness: 30  $\mu\text{m}$ ; substrate thickness: 25  $\mu\text{m}$ ) which has a thermal shrinkage of 0.5% or less at 200°C and, when adhered to a leadframe, shows an initial pressure-sensitive adhesive strength of 500 gf/20 mm at 23°C, a pressure-sensitive adhesive strength of 550 gf/20 mm after 200°C x 1 hour, and a pressure-sensitive adhesive strength of 550 gf/20 mm after 200°C x 5 hours. Because of the higher pressure-sensitive adhesive strength of the pressure-sensitive adhesive tape than that of Example 1, the leadframe was slightly deformed on peeling the adhesive tape, but no resin leak occurred.

This Example lends confirmation to the advantage of using a pressure-sensitive adhesive tape having an adhesive strength of 400 gf/20 mm or less after resin encapsulating.

Example 3

Resin encapsulating was carried out in the same manner as in Example 1, except for using a silicone pressure-sensitive adhesive tape (pressure-sensitive adhesive layer thickness: 10  $\mu\text{m}$ ; substrate thickness: 25  $\mu\text{m}$ ) which had a thermal shrinkage of 2% or less at 200°C and, when adhered to a leadframe, shows an initial

pressure-sensitive adhesive strength of 220 gf/20 mm at 23°C, a pressure-sensitive adhesive strength of 300 gf/20 mm after 200°C x 1 hour, and a pressure-sensitive adhesive strength of 300 gf/20 mm after 200°C x 5 hours. The same results as in Example 1 were obtained.

#### Comparative Example 2

Resin encapsulating was carried out in the same manner as in Example 1, except for using an available silicone pressure-sensitive adhesive tape (pressure-sensitive adhesive layer thickness: 30  $\mu$ m; substrate thickness: 25  $\mu$ m) which has a thermal shrinkage of 7.5% at 200°C and, when adhered to a leadframe, shows an initial pressure-sensitive adhesive strength of 700 gf/20 mm at 23°C, a pressure-sensitive adhesive strength of 750 gf/20 mm after 200°C x 1 hour, and a pressure-sensitive adhesive strength of 750 gf/20 mm after 200°C x 5 hours. As a result, resin leaks occurred, and the leadframe underwent deformation during resin encapsulating. The above results prove the significance of using a pressure-sensitive adhesive tape having a thermal shrinkage of 3% or less on resin encapsulating.

#### Industrial Applicability

When semiconductor chips are each bonded to a leadframe or a tape carrier and encapsulated with a resin in a mold, the present invention makes it feasible to sufficiently reduce the area of the contact interface between the leadframe, etc. and the mold. As a result, the number of cavities per mold can be increased so that the number of chips encapsulated in a single shot can be increased,

leading to improved efficiency in resin encapsulating.

Further, the present invention satisfactorily prevents breakage of bonding parts during resin encapsulating, providing an excellent yield in resin encapsulating.

- 5           Furthermore, the present invention prevents foreign matter's entering the stitches of a leadframe, development of scratches, dusting due to resin leaks, and so forth, leading to improved resin encapsulating quality.

## CLAIMS

1. A resin encapsulating method for a semiconductor chip comprising adhering a pressure-sensitive adhesive tape to a  
5 leadframe, bonding a semiconductor chip to the leadframe having the pressure-sensitive adhesive tape adhered thereto, encapsulating the semiconductor chip with a resin in a mold, and stripping the pressure-sensitive adhesive tape, wherein the pressure-sensitive adhesive tape has a thermal shrinkage of 3% or  
10 less on resin encapsulating.
2. A resin encapsulating method for a semiconductor chip according to claim 1, wherein a tape carrier film is used in place of the leadframe.
3. A pressure-sensitive adhesive tape to be adhered to a  
15 leadframe and the like, which is used in the resin encapsulating method for a semiconductor chip according to claim 1 or 2 and has a thermal shrinkage of 3% or less on resin encapsulating.

# ABSTRACT

A pressure-sensitive adhesive tape (2) is adhered to a leadframe (1). A semiconductor chip (3) is bonded to the leadframe (A) having the pressure-sensitive adhesive tape and encapsulated with a resin in a mold (4). The pressure-sensitive adhesive tape (2) is stripped after the encapsulating. The resin encapsulating efficiency is improved, and breakage of bonding parts is prevented with certainty.

1/2

FIG.1A

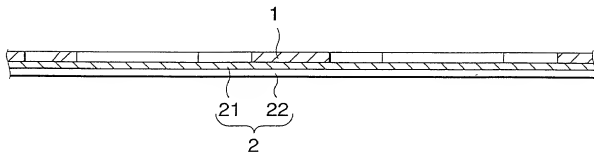


FIG.1B

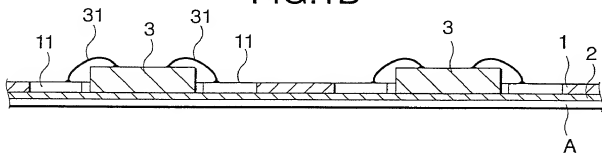


FIG.1C

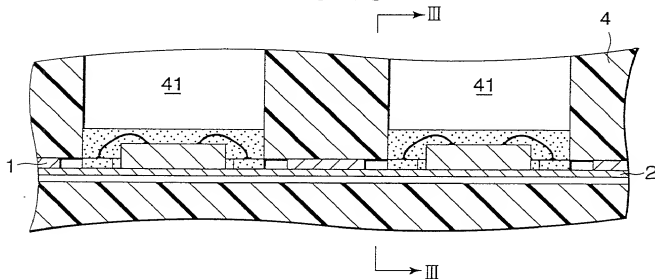


FIG.1D

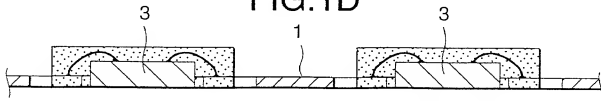
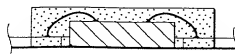


FIG.1E



2/2

FIG.2

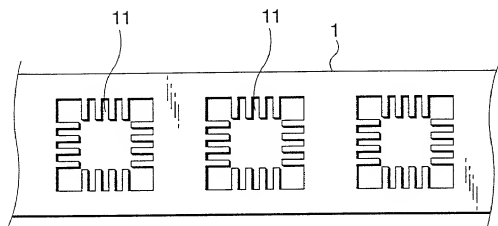
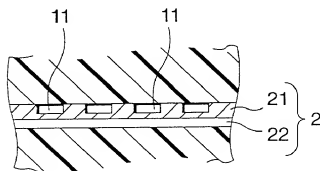


FIG.3



# DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

## METHOD OF RESIN ENCAPSULATING SEMICONDUCTOR CHIP AND PRESSURE-SENSITIVE ADHESIVE TAPE FOR ADHESION TO LEADFRAME AND THE LIKE

the specification of which is attached hereto unless the following box is checked:

☐ was filed on \_\_\_\_\_ as United States Application Number or PCT International Application Number \_\_\_\_\_ and was amended on \_\_\_\_\_ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information of which is material to the patentability as defined in 37 CFR § 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate, or § 365(a) of any PCT International application which designated at least one country other than United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application(s)

Priority Not Claimed

P. Hei. 11-103559

Japan

12/04/1999

☐

(Number)

(Country)

(Day/Month/Year Filed)

(Number)

(Country)

(Day/Month/Year Filed)

(Number)

(Country)

(Day/Month/Year Filed)

☐

☐

I hereby claim the benefits under 35 U.S.C. § 119(e) of any United States provisional application(s) listed below.

(Application Number)

(Filing Date)

I hereby claim the benefits under 35 U.S.C. § 120 of any United States application(s), or § 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of 35 U.S.C. § 112, I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR § 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of this application.

(Application Number)

(Filing Date)

(Status - patented, pending, abandoned)

(Application Number)

(Filing Date)

(Status - patented, pending, abandoned)

I hereby appoint John H. Mion, Reg. No. 18,879; Thomas J. Macpeak, Reg. No. 19,292; Robert J. Seas, Jr., Reg. No. 21,092; Darryl Mexic, Reg. No. 23,063; Robert V. Sloan, Reg. No. 22,775; Peter D. Olexy, Reg. No. 24,513; J. Frank Osha, Reg. No. 24,625; Waddell A. Biggart, Reg. No. 24,861; Louis Gubinsky, Reg. No. 24,835; Neil B. Siegel, Reg. No. 25,200; David J. Cushing, Reg. No. 28,703; John R. Inge, Reg. No. 26,916; Joseph J. Ruch, Jr., Reg. No. 26,577; Sheldon I. Landsman, Reg. No. 25,430; Richard C. Turner, Reg. No. 29,719; Howard L. Bernstein, Reg. No. 25,656; Alan J. Kasper, Reg. No. 25,425; Kenneth J. Burchfiel, Reg. No. 31,333; Gordon K. Reg. No. 30,764; Susan J. Mack, Reg. No. 30,351; Frank L. Bernstein, Reg. No. 31,484; Mark Boland, Reg. No. 32,152; William H. Mandir, Reg. No. 32,156; Scott M. Daniels, Reg. No. 32,562; Brian W. Hannan, Reg. No. 32,778; Abraham J. Rosner, Reg. No. 33,276; Bruce E. Kramer, Reg. No. 33,725; Paul F. Neils, Reg. No. 33,102; Brett S. Sylvester, Reg. No. 32,765 and Robert M. Masters, Reg. No. 35,603; my attorneys to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith, and request that all correspondence about the application be addressed to SUGHRUE, MIUN, ZINN, MACPEAK & SEAS, PLLC, 2100 Pennsylvania Avenue, N.W., Washington, D.C. 20037.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Date December 4, 2000

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